

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) A light emitting diode bar system comprising:
an array of light emitting diodes on a substrate;
a control unit coupled to the array of light emitting diodes, the control unit comprising a data formatting unit and a clock circuit that outputs a clock output signal that enables properly timed activation of individual light emitting diodes of the array of light emitting diodes;
the control unit further including or coupled to a spread spectrum clock generator that generates the clock output signal, wherein the clock output signal has reduced amplitude electromagnetic interference spectral components such that electromagnetic interference emissions from the array of light emitting diodes are reduced.
2. (Previously Presented) The light emitting diode bar system according to claim 1, wherein the clock circuit is coupled to an oscillator that is coupled to the spread spectrum clock generator, wherein the oscillator generates a reference frequency signal such that the clock output signal generated by the spread spectrum clock generator has a fundamental frequency and reduced amplitude EMI spectral components at harmonics of the fundamental frequency.
3. (Original) The light emitting diode bar system according to claim 1, wherein the spread spectrum clock generator comprises:
a clock pulse generator; and
a spread spectrum modulator.

4. (Original) The light emitting diode bar system according to claim 3, wherein the spread spectrum modulator is a frequency modulator.

5. (Original) The light emitting diode bar system according to claim 4, wherein the frequency modulator is a profile modulator for modulating the clock pulse generator with a periodic waveform.

6. (Original) The light emitting diode bar system according to claim 3, wherein the spread spectrum modulator varies up and down at an asynchronous rate to a clock strobe pulse.

7. (Original) The light emitting diode bar system according to claim 1, wherein the system includes at least two arrays of light emitting diodes.

8. (Previously Presented) A light emitting diode bar system comprising:
an array of light emitting diodes on a substrate;
a control unit coupled to the array of light emitting diodes, the control unit comprising a data formatting unit and a clock circuit that outputs a clock output signal that enables properly timed activation of individual light emitting diodes of the array of light emitting diodes; and

the array of light emitting diodes including or coupled to a spread spectrum clock generator that generates a spread spectrum output signal having reduced amplitude electromagnetic interference spectral components such that electromagnetic interference emissions from the array of light emitting diodes are reduced.

9. (Previously Presented) An image forming device including the light emitting diode bar system of claim 1, and further having an electrically chargeable photoreceptor on which a latent image of an original image is formed by the light emitting diode array, which latent image is subsequently developed with toner and then transferred to a transfer surface.

10. (Previously Presented) The image forming device according to claim 9, wherein the clock circuit is coupled to an oscillator that is coupled to the spread spectrum clock generator, wherein the oscillator generates a reference frequency signal such that the clock output signal generated by the spread spectrum clock generator has a fundamental frequency and reduced amplitude EMI spectral components at harmonics of the fundamental frequency.

11. (Previously Presented) The light emitting diode bar system according to claim 8, wherein the spread spectrum clock generator comprises:

- a clock pulse generator; and
- a spread spectrum modulator.

12. (Previously Presented) The light emitting diode bar system according to claim 11, wherein the spread spectrum modulator is a frequency modulator.

13. (Previously Presented) The light emitting diode bar system according to claim 12, wherein the frequency modulator is a profile modulator for modulating the clock pulse generator with a periodic waveform.

14. (Previously Presented) The light emitting diode bar system according to claim 12, wherein the spread spectrum modulator varies up and down at an asynchronous rate to a clock strobe pulse.

15. (Previously Presented) A method of reducing electromagnetic interference emissions from a light emitting diode bar system of an image forming device, wherein the light emitting diode bar system comprises

- an array of light emitting diodes on a substrate; and
- a control unit including or coupled to a data formatting unit and a clock circuit that outputs a clock output signal with reduced amplitude electromagnetic interference

spectral components, the method comprising modulating a frequency of the clock circuit to spread electromagnetic emissions over a range of frequencies such that electromagnetic interference emissions from the array of light emitting diodes are reduced.

16. (Original) The method of reducing electromagnetic interference emissions from a light emitting diode bar system of an image forming device according to claim 15, wherein the frequency is modulated using a periodic waveform.

17. (Original) The method of reducing electromagnetic interference emissions from a light emitting diode bar system of an image forming device according to claim 15, wherein the frequency is modulated up and down at an asynchronous rate.

18. (Previously Presented) An image forming device including the light emitting diode bar system of claim 8, and further having an electrically chargeable photoreceptor on which a latent image of an original image is formed by the light emitting diode array, which latent image is subsequently developed with toner and then transferred to a transfer surface.

19. (New) The light emitting diode bar system according to claim 1, wherein the data formatting unit and the clock circuit are coupled to the array of light emitting diodes, and the spread spectrum clock generator is coupled to the data formatting unit and the clock circuit.

20. (New) The light emitting diode bar system according to claim 8, wherein the data formatting unit and the clock circuit are coupled to the array of light emitting diodes.